

# WI180C-IOA00

Interface modules

**INTEGRATION MODULES AND ADAPTERS** 





#### Ordering information

Туре	part no.
WI180C-IOA00	6071650

Other models and accessories → www.sick.com/Interface\_modules



#### Detailed technical data

#### **Features**

Product segment	Integration modules and adapters
Product	Interface modules
Description	IO-Link Smart Sensor Gateway for WLL180T, KTL180 and AOD1; Features: IO-Link; COM3; M8 connection, 4-pin; full read/write functionality for the process and service data of the connected sensors. See operating instructions for additional information and technical details
Enclosure rating	IP50
Dimensions (W x H x L)	39 mm x 36.3 mm x 102.35 mm

#### Certificates

EU declaration of conformity	✓
UK declaration of conformity	✓
ACMA declaration of conformity	✓
Moroccan declaration of conformity	✓
China RoHS	✓
cULus certificate	✓
Information according to Art. 3 of Data Act (Regulation EU 2023/2854)	✓

#### Classifications

ECLASS 5.0	27242202
ECLASS 5.1.4	27242602
ECLASS 6.0	27242602
ECLASS 6.2	27242602
ECLASS 7.0	27242602
ECLASS 8.0	27242602
ECLASS 8.1	27242602
ECLASS 9.0	27242602

# WI180C-IOA00 | Interface modules INTEGRATION MODULES AND ADAPTERS

ECLASS 10.0	27242602
ECLASS 11.0	27242602
ECLASS 12.0	27242602
ETIM 5.0	EC001597
ETIM 6.0	EC001597
ETIM 7.0	EC001597
ETIM 8.0	EC001597
UNSPSC 16.0901	32151705

#### Technical data

Features	Supported products	WLL180T fiber amplifiers
	Supported products	KTL180 fiber contrast sensors
		OD1 displacement sensors (via AOD1 amplifier)
		OL1 displacement sensors (via AOD1 amplifier)
	Further functions	IO-Link connection for easy configuration of the WI180C-IO Sensor
		Integration Gateway with SOPAS ET, the engineering tool from SICK
Mechanics/ Electronics	Supply voltage	10.8 - 26.4 V (SIO), 18 - 26.4 V (IO-Link)
	Power consumption (without connected devices)	40 mA
	Switch-on delay	300 ms (connected to one WLL180T/KTL180)
	Switch-on delay (overall system)	350 ms (connected to 16 WLL180T/KTL180)
		2.5 s (connected to one AOD1 with OD1)
	Optical indicators	1 Green (Power/C)
		2 Orange (Q1, Q2)
	Switching output	Push/Pull
	Output: QL1 / C	Switching output or IO-Link mode
	Output current Imax	≤ 100 mA <sup>6</sup>
	Pin 2 configuration	External input, Teach-in, switching signal
	Connection type	Male connector M8, 4-pin
	Circuit protection	A <sup>2</sup>
		B <sup>3</sup>
		C <sup>4</sup>
	D 4 C 1	E
	Protection class	
	Weight	20 g
	Dimensions (W x H x D)	10.5 mm x 34.6 mm x 71.9 mm
	Housing material	Plastic, ABS/PC
	Enclosure rating	IP50 <sup>S</sup>
Safety-related parameters	MTTFd	520.7 years
, p	DCavg	0%
Smart Task	Smart Task name	Base logics (SLTI)
Smart rask		Direct
	Logic function	
		AND
		OR
		Window
		Hysteresis
	Timer function	Deactivated
		On delay
		Off delay
		ON and OFF delay
		Impulse (one shot)
	Inverter	Yes
IO-Link interface	Protocol version	IO-Link V1.1
	Communication interface detail	COM3 (230.4 kBaud)
	Cycle time	2.0 ms
	Process data length	16 hytes
	Process data length	16 bytes
	Process data structure	Bit 0 - Bit 1 = QL1 - QL2
		Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2
	Process data structure	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 = Reserved
	Process data structure	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 = Reserved Bit 48 - Bit 63 = Analog output 5
	Process data structure	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 = Reserved
	Process data structure	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 = Reserved Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4
	Process data structure	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 = Reserved Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 95 = Analog output 3
	Process data structure	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 - Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 = Reserved Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 95 = Analog output 3 Bit 96 - Bit 111 = Analog output 2
	Process data structure (5 analog outputs out of 16 modules)	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 = Reserved Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 95 = Analog output 3 Bit 96 - Bit 111 = Analog output 2 Bit 112 - Bit 112 = Analog output 2 Bit 112 - Bit 112 = Analog output 1
	Process data structure (5 analog outputs out of 16 modules)  Process data structure	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 = Reserved Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 95 = Analog output 3 Bit 96 - Bit 111 = Analog output 2 Bit 112 - Bit 127 = Analog output 2 Bit 112 - Bit 127 = Analog output 1
	Process data structure (5 analog outputs out of 16 modules)	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 = Reserved Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 95 = Analog output 3 Bit 96 - Bit 111 = Analog output 2 Bit 112 - Bit 127 = Analog output 1 Bit 0 - Bit 1 27 = Analog output 1 Bit 0 - Bit 1 27 = Module 1 Qint.1 - Module 7 Qint.2 Bit 2 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2
	Process data structure (5 analog outputs out of 16 modules)  Process data structure	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 = Reserved Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 95 = Analog output 3 Bit 96 - Bit 111 = Analog output 2 Bit 112 - Bit 127 = Analog output 1 Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2 Bit 16 - Bit 31 = Analog output 7
	Process data structure (5 analog outputs out of 16 modules)  Process data structure	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 = Reserved Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 195 = Analog output 3 Bit 96 - Bit 111 = Analog output 2 Bit 111 = Analog output 2 Bit 112 - Bit 127 = Analog output 1 Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2 Bit 16 - Bit 31 = Analog output 7 Bit 32 - Bit 47 = Analog output 7
	Process data structure (5 analog outputs out of 16 modules)  Process data structure	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 = Reserved Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 95 = Analog output 3 Bit 96 - Bit 111 = Analog output 2 Bit 112 - Bit 127 = Analog output 1 Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2 Bit 16 - Bit 31 = Analog output 7
	Process data structure (5 analog outputs out of 16 modules)  Process data structure	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 = Reserved Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 195 = Analog output 3 Bit 96 - Bit 111 = Analog output 2 Bit 111 = Analog output 2 Bit 112 - Bit 127 = Analog output 1 Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2 Bit 16 - Bit 31 = Analog output 7 Bit 32 - Bit 47 = Analog output 7
	Process data structure (5 analog outputs out of 16 modules)  Process data structure	Bit 0 - Bit 1 = QL1 - QL2   Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2   Bit 34 - Bit 47 = Reserved   Bit 48 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 4   Bit 80 - Bit 95 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 2   Bit 112 - Bit 127 = Analog output 1   Bit 0 - Bit 1 = QL1 - QL2   Bit 2 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2   Bit 16 - Bit 31 = Analog output 7   Bit 32 - Bit 47 = Analog output 6   Bit 48 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 5   Bit 64 - Bit 79 = Analog output 5   Bit 64 - Bit 79 = Analog output 4
	Process data structure (5 analog outputs out of 16 modules)  Process data structure	Bit 0 - Bit 1 = QL1 - QL2
	Process data structure (5 analog outputs out of 16 modules)  Process data structure	Bit 0 - Bit 1 = QL1 - QL2   Bit 2 - Bit 33 - Module 1 Qint.1 - Module 16 Qint.2   Bit 34 - Bit 47 = Reserved   Bit 48 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 4   Bit 80 - Bit 95 = Analog output 2   Bit 101 - Bit 127 = Analog output 2   Bit 112 - Bit 127 = Analog output 1   Bit 0 - Bit 1 = QL1 - QL2   Bit 16 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2   Bit 16 - Bit 31 = Analog output 7   Bit 32 - Bit 47 = Analog output 6   Bit 48 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 4   Bit 80 - Bit 95 = Analog output 4   Bit 80 - Bit 95 = Analog output 3   Bit 96 - Bit 111 = Analog output 3   Bit 96 - Bit 111 = Analog output 2
	Process data structure (5 analog outputs out of 16 modules)  Process data structure (7 analog outputs out of 7 modules)	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 = Reserved Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 95 = Analog output 3 Bit 96 - Bit 111 = Analog output 2 Bit 112 - Bit 127 = Analog output 2 Bit 112 - Bit 127 = Analog output 1 Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2 Bit 61 - Bit 31 = Analog output 7 Bit 32 - Bit 47 = Analog output 6 Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 95 = Analog output 4 Bit 80 - Bit 95 = Analog output 2 Bit 11 = Bit 127 = Analog output 2 Bit 11 = Bit 127 = Analog output 2 Bit 11 = Bit 127 = Analog output 2 Bit 11 = Bit 127 = Analog output 2 Bit 11 = Bit 127 = Analog output 2
	Process data structure (5 analog outputs out of 16 modules)  Process data structure (7 analog outputs out of 7 modules)  Vendor ID	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 - Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 = Reserved Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 95 = Analog output 3 Bit 96 - Bit 111 = Analog output 2 Bit 112 - Bit 127 = Analog output 1 Bit 0 - Bit 1 = QL1 - QL2 Bit 112 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2 Bit 16 - Bit 31 = Analog output 7 Bit 2- Bit 15 = Module 1 Qint.1 - Module 7 Qint.2 Bit 16 - Bit 31 = Analog output 6 Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 5 Bit 64 - Bit 95 = Analog output 3 Bit 80 - Bit 95 = Analog output 3 Bit 96 - Bit 111 = Analog output 2 Bit 112 - Bit 127 = Analog output 2 Bit 112 - Bit 127 = Analog output 2 Bit 112 - Bit 127 = Analog output 2 Bit 112 - Bit 127 = Analog output 1
	Process data structure (5 analog outputs out of 16 modules)  Process data structure (7 analog outputs out of 7 modules)  Vendor ID Device ID HEX	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 = Reserved Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 95 = Analog output 4 Bit 80 - Bit 95 = Analog output 2 Bit 112 - Bit 127 = Analog output 2 Bit 112 - Bit 127 = Analog output 1 Bit 0 - Bit 1 = QL1 - QL2 Bit 12 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2 Bit 16 - Bit 31 = Analog output 7 Bit 32 - Bit 47 = Analog output 6 Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 95 = Analog output 3 Bit 96 - Bit 111 = Analog output 2 Bit 112 - Bit 127 = Analog output 2 Bit 112 - Bit 127 = Analog output 1
	Process data structure (5 analog outputs out of 16 modules)  Process data structure (7 analog outputs out of 7 modules)  Vendor ID  Device ID HEX  Device ID DEC	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 = Reserved Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 95 = Analog output 3 Bit 96 - Bit 1111 = Analog output 2 Bit 1112 - Bit 127 = Analog output 1 Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2 Bit 16 - Bit 31 = Analog output 7 Bit 32 - Bit 47 = Analog output 7 Bit 32 - Bit 63 = Analog output 6 Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 95 = Analog output 4 Bit 80 - Bit 95 = Analog output 2 Bit 110 - Bit 127 = Analog output 2 Bit 110 - Bit 127 = Analog output 1 Dit 110 - Bit 127 = Analog output 1 Dit 110 - Bit 127 = Analog output 1 Dit 110 - Bit 127 = Analog output 1 Dit 110 - Bit 127 = Analog output 1 Dit 110 - Bit 127 = Analog output 1 Dit 110 - Bit 127 = Analog output 1
Internal system bus	Process data structure (5 analog outputs out of 16 modules)  Process data structure (7 analog outputs out of 7 modules)  Vendor ID Device ID HEX Device ID DEC Protocol version	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 = Reserved Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 95 = Analog output 4 Bit 80 - Bit 95 = Analog output 2 Bit 112 - Bit 127 = Analog output 2 Bit 112 - Bit 127 = Analog output 1 Bit 0 - Bit 1 = QL1 - QL2 Bit 12 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2 Bit 16 - Bit 31 = Analog output 7 Bit 32 - Bit 47 = Analog output 6 Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 95 = Analog output 3 Bit 96 - Bit 111 = Analog output 2 Bit 112 - Bit 127 = Analog output 2 Bit 112 - Bit 127 = Analog output 1
Internal system bus	Process data structure (5 analog outputs out of 16 modules)  Process data structure (7 analog outputs out of 7 modules)  Vendor ID  Device ID HEX  Device ID DEC	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 = Reserved Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 95 = Analog output 3 Bit 96 - Bit 1111 = Analog output 2 Bit 1112 - Bit 127 = Analog output 1 Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2 Bit 16 - Bit 31 = Analog output 7 Bit 32 - Bit 47 = Analog output 7 Bit 32 - Bit 63 = Analog output 6 Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 95 = Analog output 4 Bit 80 - Bit 95 = Analog output 2 Bit 110 - Bit 127 = Analog output 2 Bit 110 - Bit 127 = Analog output 1 Dit 110 - Bit 127 = Analog output 1 Dit 110 - Bit 127 = Analog output 1 Dit 110 - Bit 127 = Analog output 1 Dit 110 - Bit 127 = Analog output 1 Dit 110 - Bit 127 = Analog output 1 Dit 110 - Bit 127 = Analog output 1
Internal system bus Ambient data	Process data structure (5 analog outputs out of 16 modules)  Process data structure (7 analog outputs out of 7 modules)  Vendor ID  Device ID HEX Device ID DEC Protocol version  Maximum number of connected modules	Bit 0 - Bit 1 = QL1 - QL2     Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2     Bit 34 - Bit 47 = Reserved     Bit 48 - Bit 63 = Analog output 5     Bit 64 - Bit 79 = Analog output 4     Bit 80 - Bit 95 = Analog output 3     Bit 96 - Bit 111 = Analog output 2     Bit 112 - Bit 127 = Analog output 2     Bit 112 - Bit 127 = Analog output 1     Bit 0 - Bit 1 = QL1 - QL2     Bit 2 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2     Bit 61 - Bit 31 = Analog output 7     Bit 32 - Bit 47 = Analog output 6     Bit 48 - Bit 63 = Analog output 5     Bit 64 - Bit 79 = Analog output 4     Bit 80 - Bit 95 = Analog output 3     Bit 96 - Bit 111 = Analog output 2     Bit 112 - Bit 127 = Analog output 2     Bit 112 - Bit 127 = Analog output 1     Cox00022E     Rind 138 - Rind 148 -
-	Process data structure (5 analog outputs out of 16 modules)  Process data structure (7 analog outputs out of 7 modules)  Vendor ID  Device ID HEX  Device ID DEC  Protocol version  Maximum number of connected modules  Electromagnetic compatibility (EMC)	Bit 0 - Bit 1 = QL1 - QL2 Bit 2 - Bit 33 - Module 1 Qint.1 - Module 16 Qint.2 Bit 34 - Bit 47 - Reserved Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 4 Bit 80 - Bit 95 = Analog output 4 Bit 80 - Bit 95 = Analog output 2 Bit 112 - Bit 127 = Analog output 1 Bit 0 - Bit 1 = QL1 - QL2 Bit 115 - Bit 127 = Analog output 1 Bit 0 - Bit 1 = QL1 - QL2 Bit 16 - Bit 31 = Analog output 7 Bit 20 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2 Bit 16 - Bit 31 = Analog output 7 Bit 32 - Bit 47 = Analog output 6 Bit 48 - Bit 63 = Analog output 5 Bit 64 - Bit 79 = Analog output 5 Bit 69 - Bit 111 = Analog output 3 Bit 90 - Bit 111 = Analog output 3 Bit 90 - Bit 111 = Analog output 2 Bit 112 - Bit 127 = Analog output 2 Bit 112 - Bit 127 = Analog output 2 Bit 112 - Bit 127 = Analog output 1 Bit 90 - Bit 91 - Analog output 1 Bit 91 - Bit 91 - Analog output 1 Bit 91 - Bit 91 - Analog output 1 Bit 91 - Bit 91 - Analog output 1 Bit 91 - Bit 91 - Analog output 1 Bit 91 - Bit 91 - Analog output 1 Bit 91 - Bit 91 - Analog output 1 Bit 91 - Bit 91 - Analog output 1 Bit 91 - Bit 91 - Analog output 1 Bit 91 - Bit 91 - Analog output 1 Bit 91 - Bit 91
-	Process data structure (5 analog outputs out of 16 modules)  Process data structure (7 analog outputs out of 7 modules)  Vendor ID  Device ID HEX  Device ID DEC  Protocol version  Maximum number of connected modules  Electromagnetic compatibility (EMC)  Shock load	Bit 0 - Bit 1 = QL1 - QL2   Bit 2 - Bit 33 - Module 1 Qint.1 - Module 16 Qint.2   Bit 34 - Bit 47 = Reserved   Bit 48 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 4   Bit 80 - Bit 95 = Analog output 2   Bit 180 - Bit 191 - Bit 127 = Analog output 2   Bit 112 - Bit 127 = Analog output 2   Bit 112 - Bit 127 = Analog output 7   Bit 0 - Bit 1 = QL1 - QL2   Bit 12 - Bit 51 = Module 1 Qint.1 - Module 7 Qint.2   Bit 61 - Bit 31 = Analog output 7   Bit 32 - Bit 47 = Analog output 7   Bit 32 - Bit 63 = Analog output 5   Bit 64 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 4   Bit 80 - Bit 95 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 1   26   Quentle 111 - Analog output 1   26   Quentle 122 - Analog output 1   26   Quentle 133 - Analog output 1   26   Quentle 143 - Analog output 1   26   Quentle 144 - Analog output 1   27   28   Quentle 144 - Analog output 1   28   Quentle 145 - Analog output 1   29   Bit 112 - Bit 127 = Analog output 1   Bit 112 - Bit 127 = Analog output 1   Bit 112 - Bit 127 = Analog output 1   Bit 112 - Bit 127 = Analog output 1   Bit 114 - Bit 64 - Analog output 2   Bit 115 - Analog output 1   Bit 115 - Analog output 2   Bit 115 - Bit 127 - Analog output 1   Bit 115 - Bit 127 - Analog output 1   Bit 115 - Bit 127 - Analog output 1   Bit 115 - Bit 127 - Analog output 1   Bit 115 - Bit 127 - Analog output 1   Bit 115 - Bit 127 - Analog output 1   Bit 115 - Bit 127 - Analog output 1   Bit 115 - Bit 127 - Analog output 1   Bit 115 - Bit 127 -
-	Process data structure (5 analog outputs out of 16 modules)  Process data structure (7 analog outputs out of 7 modules)  Vendor ID  Device ID HEX  Device ID DEC  Protocol version  Maximum number of connected modules  Electromagnetic compatibility (EMC)  Shock load  Ambient operating temperature	Bit 0 - Bit 1 = QL1 - QL2   Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2   Bit 34 - Bit 47 = Reserved   Bit 48 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 4   Bit 80 - Bit 95 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 2   Bit 112 - Bit 127 = Analog output 1   Bit 0 - Bit 1 = QL1 - QL2   Bit 2 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2   Bit 16 - Bit 31 = Analog output 7   Bit 32 - Bit 47 = Analog output 6   Bit 48 - Bit 63 = Analog output 6   Bit 48 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 4   Bit 80 - Bit 95 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 2   Bit 112 - Bit 127 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 1   C6   Ox80022E   3389166   V1   BIT 16   EN 60947-5-2   S00m/s² (SOG)   -25 +55°C <sup>6</sup>
-	Process data structure (5 analog outputs out of 16 modules)  Process data structure (7 analog outputs out of 7 modules)  Vendor ID  Device ID HEX  Device ID DEC  Protocol version  Maximum number of connected modules Electromagnetic compatibility (EMC)  Shock load  Ambient operating temperature  Ambient storage temperature	Bit 0 - Bit 1 = QL1 - QL2   Bit 2 - Bit 33 - Module 1 Qint.1 - Module 16 Qint.2   Bit 34 - Bit 47 = Reserved   Bit 48 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 4   Bit 80 - Bit 95 = Analog output 2   Bit 180 - Bit 191 - Bit 127 = Analog output 2   Bit 112 - Bit 127 = Analog output 2   Bit 112 - Bit 127 = Analog output 7   Bit 0 - Bit 1 = QL1 - QL2   Bit 12 - Bit 51 = Module 1 Qint.1 - Module 7 Qint.2   Bit 61 - Bit 31 = Analog output 7   Bit 32 - Bit 47 = Analog output 7   Bit 32 - Bit 63 = Analog output 5   Bit 64 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 4   Bit 80 - Bit 95 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 1   26   Quentle 111 - Analog output 1   26   Quentle 122 - Analog output 1   26   Quentle 133 - Analog output 1   26   Quentle 143 - Analog output 1   26   Quentle 144 - Analog output 1   27   28   Quentle 144 - Analog output 1   28   Quentle 145 - Analog output 1   29   Bit 112 - Bit 127 = Analog output 1   Bit 112 - Bit 127 = Analog output 1   Bit 112 - Bit 127 = Analog output 1   Bit 112 - Bit 127 = Analog output 1   Bit 114 - Bit 64 - Analog output 2   Bit 115 - Analog output 1   Bit 115 - Analog output 2   Bit 115 - Bit 127 - Analog output 1   Bit 115 - Bit 127 - Analog output 1   Bit 115 - Bit 127 - Analog output 1   Bit 115 - Bit 127 - Analog output 1   Bit 115 - Bit 127 - Analog output 1   Bit 115 - Bit 127 - Analog output 1   Bit 115 - Bit 127 - Analog output 1   Bit 115 - Bit 127 - Analog output 1   Bit 115 - Bit 127 -
-	Process data structure (5 analog outputs out of 16 modules)  Process data structure (7 analog outputs out of 7 modules)  Vendor ID  Device ID DEX  Device ID DEX  Device ID DEX  Device ID DEX  Electromagnetic compatibility (EMC)  Shock load  Ambient operating temperature  Ambient storage temperature	Bit 0 - Bit 1 = QL1 - QL2   Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2   Bit 34 - Bit 47 = Reserved   Bit 48 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 4   Bit 80 - Bit 95 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 2   Bit 112 - Bit 127 = Analog output 1   Bit 0 - Bit 1 = QL1 - QL2   Bit 2 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2   Bit 16 - Bit 31 = Analog output 7   Bit 32 - Bit 47 = Analog output 6   Bit 48 - Bit 63 = Analog output 6   Bit 48 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 4   Bit 80 - Bit 95 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 2   Bit 112 - Bit 127 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 1   C6   Ox80022E   3389166   V1   BIT 16   EN 60947-5-2   S00m/s² (SOG)   -25 +55°C <sup>6</sup>
-	Process data structure (5 analog outputs out of 16 modules)  Process data structure (7 analog outputs out of 7 modules)  Vendor ID  Device ID DEX  Device ID DEX  Device ID DEX  Device ID DEX  Electromagnetic compatibility (EMC)  Shock load  Ambient operating temperature  Ambient storage temperature	Bit 0 - Bit 1 = QL1 - QL2   Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2   Bit 34 - Bit 47 = Reserved   Bit 48 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 4   Bit 80 - Bit 95 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 2   Bit 112 - Bit 127 = Analog output 1   Bit 0 - Bit 1 = QL1 - QL2   Bit 2 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2   Bit 16 - Bit 31 = Analog output 7   Bit 32 - Bit 47 = Analog output 6   Bit 48 - Bit 63 = Analog output 6   Bit 48 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 4   Bit 80 - Bit 95 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 2   Bit 112 - Bit 127 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 1   C6   Ox80022E   3389166   V1   BIT 16   EN 60947-5-2   S00m/s² (SOG)   -25 +55°C <sup>6</sup>
-	Process data structure (5 analog outputs out of 16 modules)  Process data structure (7 analog outputs out of 7 modules)  Vendor ID  Device ID HEX  Device ID DEC  Protocol version  Maximum number of connected modules Electromagnetic compatibility (EMC) Shock load  Ambient operating temperature  Ambient storage temperature  Ambient storage temperature  3 B = inputs and output reverse-polarity protected. 3 B = inputs and output reverse-polarity protected.	Bit 0 - Bit 1 = QL1 - QL2   Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2   Bit 34 - Bit 47 = Reserved   Bit 48 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 4   Bit 80 - Bit 95 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 2   Bit 112 - Bit 127 = Analog output 1   Bit 0 - Bit 1 = QL1 - QL2   Bit 2 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2   Bit 16 - Bit 31 = Analog output 7   Bit 32 - Bit 47 = Analog output 6   Bit 48 - Bit 63 = Analog output 6   Bit 48 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 4   Bit 80 - Bit 95 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 2   Bit 112 - Bit 127 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 1   C6   Ox80022E   3389166   V1   BIT 16   EN 60947-5-2   S00m/s² (SOG)   -25 +55°C <sup>6</sup>
-	Process data structure (5 analog outputs out of 16 modules)  Process data structure (7 analog outputs out of 7 modules)  Vendor ID  Device ID HEX  Device ID DEC  Protocol version  Maximum number of connected modules  Electromagnetic compatibility (EMC)  Shock load  Ambient storage temperature <sup>2</sup> A = VS connections reverse-polarity protected. <sup>3</sup> B = inputs and output reverse-polarity protected. <sup>4</sup> C = interference suppression.	Bit 0 - Bit 1 = QL1 - QL2   Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2   Bit 34 - Bit 47 = Reserved   Bit 48 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 4   Bit 80 - Bit 95 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 2   Bit 112 - Bit 127 = Analog output 1   Bit 0 - Bit 1 = QL1 - QL2   Bit 2 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2   Bit 16 - Bit 31 = Analog output 7   Bit 32 - Bit 47 = Analog output 6   Bit 48 - Bit 63 = Analog output 6   Bit 48 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 4   Bit 80 - Bit 95 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 2   Bit 112 - Bit 127 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 1   C6   Ox80022E   3389166   V1   BIT 16   EN 60947-5-2   S00m/s² (SOG)   -25 +55°C <sup>6</sup>
-	Process data structure (5 analog outputs out of 16 modules)  Process data structure (7 analog outputs out of 7 modules)  Vendor ID  Device ID HEX  Device ID DEC  Protocol version  Maximum number of connected modules Electromagnetic compatibility (EMC) Shock load  Ambient operating temperature  Ambient storage temperature  Ambient storage temperature  3 B = inputs and output reverse-polarity protected. 3 B = inputs and output reverse-polarity protected.	Bit 0 - Bit 1 = QL1 - QL2   Bit 2 - Bit 33 = Module 1 Qint.1 - Module 16 Qint.2   Bit 34 - Bit 47 = Reserved   Bit 48 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 4   Bit 80 - Bit 95 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 2   Bit 112 - Bit 127 = Analog output 1   Bit 0 - Bit 1 = QL1 - QL2   Bit 2 - Bit 15 = Module 1 Qint.1 - Module 7 Qint.2   Bit 16 - Bit 31 = Analog output 7   Bit 32 - Bit 47 = Analog output 6   Bit 48 - Bit 63 = Analog output 6   Bit 48 - Bit 63 = Analog output 5   Bit 64 - Bit 79 = Analog output 4   Bit 80 - Bit 95 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 2   Bit 112 - Bit 127 = Analog output 3   Bit 96 - Bit 111 = Analog output 2   Bit 112 - Bit 127 = Analog output 1   C6   Ox80022E   3389166   V1   BIT 16   EN 60947-5-2   S00m/s² (SOG)   -25 +55°C <sup>6</sup>

#### Dimensions in mm (inch)

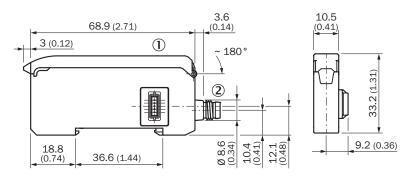
Opending on number of connected sensor devices.

1-3 units: Ambient temperature -25...55°C; max. output current 100mA

4-8 units: Ambient temperature -25...55°C; max. output current 50mA

9-16 units: Ambient temperature -25...45°C; max. output current 20mA

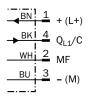
#### **Dimensional drawing**





Dimensions in mm (inch)

#### Anschlussschema Cd-447



### SICK AT A GLANCE

SICK is one of the leading manufacturers of intelligent sensors and sensor solutions for industrial applications. A unique range of products and services creates the perfect basis for controlling processes securely and efficiently, protecting individuals from accidents and preventing damage to the environment.

We have extensive experience in a wide range of industries and understand their processes and requirements. With intelligent sensors, we can deliver exactly what our customers need. In application centers in Europe, Asia and North America, system solutions are tested and optimized in accordance with customer specifications. All this makes us a reliable supplier and development partner.

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For us, that is "Sensor Intelligence."

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